

OFFICE OF THE UNIVERSITY, JNANASAHYADRI, SHANKARAGHATTA-577451 <u>DISTANCE EDUCATION</u>

POST-GARDUATE and UNDER-GRADUATE DIPLOMA COURSES in POWER ELECTRONICS

THEORY EXAMINATION – DECEMBER 2017 TIME-TABLE

TIME: 02:00 PM TO 05:00 PM

Date & Day	Paper Code New – (Old)	Paper Title	
Post-Graduation Diploma in Power Electronics (One Year)			
13-12-2017 Wednesday	59101 (PEA110)	Power Electronics I Converters	
15-12-2017 Friday	59102 (PEA120)	Power Electronics II- Inverters and Drives	
17-12-2017 Sunday	59103 (PEA130)	Power Enhancement Techniques	

Di	Diploma in Power Electronics (First Year)		
13-12-2017 Wednesday	54201 (DEA110)	Fundamental of Electricals-I	
15-12-2017 Friday	54202 (DEA120)	Fundamental of Electricals -II	
17-12-2017 Sunday	54203 (DEA130)	Fundamental of Electronics-I	

Diploma in Power Electronics (Second Year)		
14-12-2017 Thursday	54211 (DEB110)	Fundamental of Electronics –II
16-12-2017 Saturday	54212 (DEB120)	Power Enhancements Techniques

NOTES: 1) Candidates should write new Q.P. Code (Question Paper Code)

- 2) Candidates are informed to follow the instructions printed on the Answer Book & Admission Ticket.
- 3) Cell phones/Mobile phones are strictly not allowed inside the examination room/hall.
- 4) For any reason if the Government unexpectedly declares a holiday on a day on which the examination is already fixed, the examination shall be held as usual unless and otherwise heard from the University.

Sd/-REGISTRAR (EVALUATION)